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APPLICANTS

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** CONTINUING DATA *****

None *TT*

** FOREIGN APPLICATIONS *****

None *TT*

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Foreign Priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>TT</i>	ISRAEL	DRAWING 2	CLAIMS 42	CLAIMS 4
Verified and Acknowledged	<i>Zayd Hanna</i> <i>TT</i> Examiner's Signature Initials				

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TITLE

Device, system and method for VLSI design analysis

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